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Practitioner's Docket No.: CAT-12945

AUG 30 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of (1st Inventor): Sorin S. Georgescu

Assignee: Catalyst Semiconductor, Inc.

Serial No.: 10/600,125-5592

Group No.: 2824

Filed: 6/20/2003

Examiner: Toan K. Le

For: "Non-Volatile Memory Integrated Circuit"

August 30, 2005

VIA FACSIMILE (571) 273-8300

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the outstanding Office Action dated August 11, 2005, in which the Examiner imposed a restriction requirement to election of invention for the above-referenced application, Applicant elects to prosecute the invention as in Species I, Claims 1-10 drawn to a non-volatile memory array with special technical features as recited in claim 1, without traverse.

Please cancel Claims 10 bis (independent) and 11-52. Applicant reserves the right to file divisional applications on the non-elected claims.

Respectfully submitted,



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CERTIFICATE OF TRANSMISSION (37 C.F.R. 1.8(a))

I hereby certify that, on the date shown below, this correspondence is being transmitted by facsimile to the Patent and Trademark Office.

Date: Aug. 30, 2005 Signature: Carrie Reddick

Carrie Reddick